VLSI-SoC 2021
October 4-8, 2021
Singapore
Nanyang Technological University

VLSI-SoC 2021 is the 29th in a series of international conferences sponsored by the International Federation for Information Processing Technical Committee 10 Working Group 5, IEEE CEDA, and IEEE CASS, which explore the state-of-the-art in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design. The purpose of VLSI-SoC is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems.

VLSI-SoC 2021 will be held under the theme "Intelligent and Secure Edge Computing," exploring the design and optimization challenges around AI, machine learning, and cybersecurity on distributed and heterogenous low-power circuit and systems powered by CMOS and beyond-CMOS technologies.

VLSI-SoC 2021 will be held in Singapore.

Topics of interest include, but are not limited to:
- Analog mixed-signal, sensors and RF
- VLSI circuits and SoC design
- Embedded systems design and software
- CAD tools and methodologies for digital IC design and optimization
- Verification, modeling and prototyping
- Design for testability, reliability and fault tolerance
- Hardware security
- Emerging technologies and new computing paradigms

Abstract registration: April 19, 2021
Full paper submission: April 26, 2021
Special session proposal: April 26, 2021
Notification of acceptance: June 21, 2021
Camera ready: July 12, 2021